

IN THE CLAIMS:

Please amend Claims 1 to 3 and 6 to 8 as follows. A marked-up version of Claims 1 to 3 and 6 to 8, showing the changes being made thereto, is attached. Note that all claims currently pending in this application are being reproduced below for the Examiner's convenience.

1. (Twice Amended) An electronic apparatus comprising:

a clock circuit that generates a clock signal having clock signal pulses generated at a predetermined cycle; and

a non-volatile memory time-keeping counter circuit for counting clock signal pulses generated by said clock circuit and storing a count of the clock signal pulses.

2. (Amended) An electronic apparatus according to Claim 1, wherein said

non-volatile memory time-keeping counter circuit comprises a ferroelectric memory.

3. (Amended) An electronic apparatus according to Claim 1, further

comprising:

a control circuit that controls said electronic apparatus,

wherein said control circuit controls said non-volatile memory time-keeping counter circuit so as to count the clock signal pulses in response to the clock signal generated by said clock signal circuit.

4. An electronic apparatus according to Claim 1, wherein said electronic apparatus comprises a camera.

5. (Amended) An electronic apparatus according to Claim 4, further comprising a control circuit that controls said electronic apparatus, and wherein said control circuit includes a central processing unit.

6. (Amended) An electronic apparatus according to Claim 1, wherein, when a power supply battery for supplying power to said electronic apparatus is replaced, said non-volatile memory time-keeping counter circuit starts counting in a state in which a predetermined value is added to the memory contents of said non-volatile memory time-keeping counter circuit.

7. (Twice Amended) An electronic apparatus comprising:
a time-keeping clock signal generation circuit that keeps time; and
a ferroelectric memory time-keeping counter circuit that forms and stores a time signal concerning time kept by said time-keeping circuit.

8. (Amended) An electronic apparatus according to Claim 7, further comprising a control circuit that controls the electronic apparatus, wherein said control circuit controls said ferroelectric memory time-keeping counter circuit so as to store the time signal for